

CALIBRATION AND TESTING ARCHITECTURE FOR RECEIVERS

Abstract

A method and apparatus are provided to generate calibration signals to
5 multiple stages in a receiver channel. The multiple stages are calibrated using
multiple calibration circuits, where a controller controls each calibration circuit.
The controller is coupled to the output of the final stage in the receiver channel
through a single comparison unit. The output from the single comparison unit is
used by the controller to calibrate each of the multiple stages.

10

"Express Mail" mailing label number: EV415949217US
Date of Deposit: March 30, 2004

This paper or fee is being deposited on the date indicated above with
the United States Postal Service pursuant to 37 CFR 1.10, and is
addressed to the Commissioner for Patents, Mail Stop Patent
Application, P.O. Box 1450, Alexandria, VA 22313-1450.